

AF/2823  
JW



**PATENT**

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

THOMAS J. SONDERMAN  
PIRAINDER LALL

Serial No.: 10/614,354

Filed: July 7, 2003

For: METHODS OF CONTROLLING  
PROPERTIES AND CHARACTERISTICS  
OF A GATE INSULATION LAYER  
BASED UPON ELECTRICAL TEST  
DATA, AND SYSTEM FOR  
PERFORMING SAME

Group Art Unit: 2823

Examiner: Khiem D. Nguyen

Atty. Dkt. No.: 2000.100800/TT5273

Customer No.: 23720

**RESPONSE TO FINAL OFFICE ACTION DATED NOVEMBER 22, 2004**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING  
37 C.F.R 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date below:

December 17, 2004

Date

  
Signature

This paper is submitted in response to the final Office Action dated November 22, 2004, for which the three-month date for response is February 22, 2005.

No fees are believed to be due in connection with the present paper. However, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required, the Director is authorized to deduct such fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT5273. In the event the monies in that account are insufficient, the Director is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.100800.